

522,913

Rec'd PCT/PTO 28 JAN 2005

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
12 February 2004 (12.02.2004)

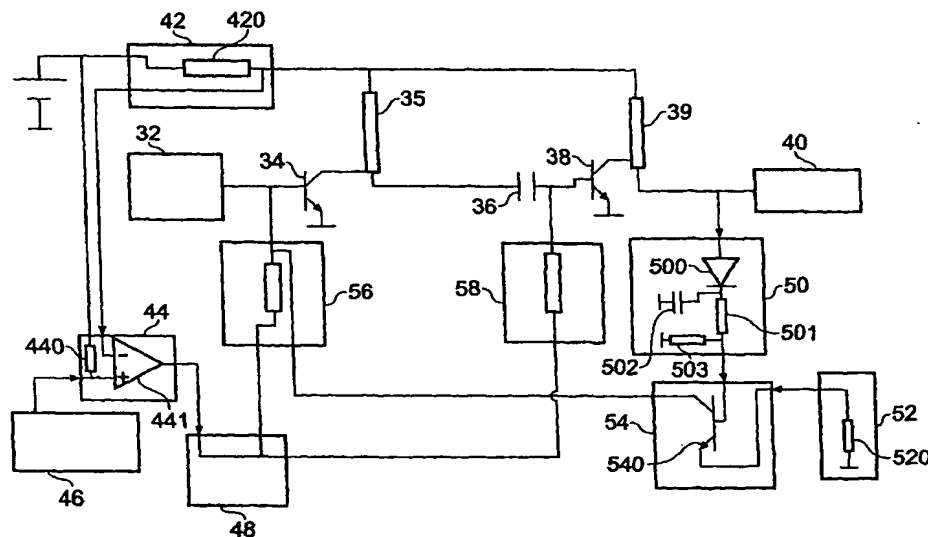
PCT

(10) International Publication Number
WO 2004/013957 A2

- (51) International Patent Classification⁷: **H03F 1/02, 1/52** (74) Agent: **O'CONNELL, David, Christopher**; Haseltine Lake, Imperial House, 15-19 Kingsway, London WC2B 6UD (US).
- (21) International Application Number: PCT/EP2003/007948
- (22) International Filing Date: 21 July 2003 (21.07.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
02255396.0 1 August 2002 (01.08.2002) EP
60/400,561 2 August 2002 (02.08.2002) US
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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- Published:
— without international search report and to be republished upon receipt of that report

[Continued on next page]

(54) Title: CIRCUIT FOR POWER AMPLIFICATION



(57) **Abstract:** A radio frequency (RF) power amplifier, including an external control loop and a protection circuit, which functions to maintain a constant output power during variations in impedance on the RF load. In the external control loop a collector current from an output transistor is detected and then regulated with respect to a reference current. The regulated signal is utilised to generate a bias control signal which is input to the base electrode of both a driver transistor and the output transistor. The protection circuit detects a voltage envelope at the collector electrode of the output transistor and utilises this signal to form a bias reduction signal which is input to the base electrode of the driver transistor.

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